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PTO/SB/08

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		<i>Complete if Known</i>	
		Application Number	09/844,670
		Filing Date	April 30, 2001
		First Named Inventor	THEKKATH
		Group Art Unit	2191
Examiner Name	Anil KHATRI		
Sheet	1 of 1	Attorney Docket Number	MTEC-011/00US

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
AK	D1	John L. Hennessy, et al., "Computer Architecture, A Quantitative Approach," <i>Morgan Kaufmann Publishers</i> , 2 nd Edition, 1996, pages 278 and 282-284.	
AK	D2	Andrew S. Tanenbaum, "Structured Computer Organization," <i>Prentice-Hall, Inc.</i> , 2 nd Edition, 1984, pages 10-12.	
AK	D3	Tom Riordan, "MIPS R3010 Floating-point Coprocessor Interface," <i>MIPS Computer Systems, Inc.</i> , October 27, 1988, pages i-ii, 1-23.	
AK	D4	LSI Logic Corporation, "LR3010 and LR3010A MIPS Floating-point Accelerator User's Manual," 1989-1991, 104 pages.	
AK	D5	LSI Logic Corporation, "LR3000 and LR3000A MIPS RISC Microprocessor User's Manual," 1990-1991, 48 pages.	
AK	D6	Rolf-Jurgen Brub, "RISC The MIPS-R3000 Family," <i>Siemens Aktiengesellschaft</i> , 1991, pages 51-65.	
AK	D7	Motorola, Inc., "MC68020 and MC68EC020 Microprocessors User's Manual, <i>Motorola</i> , 1 st Edition, 1992, pages 7-1- to 7-14.	

Examiner Signature	/Anil Khatri,	Date Considered	07/20/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number.

² Applicant is to place a check mark here if English language Translation attached.